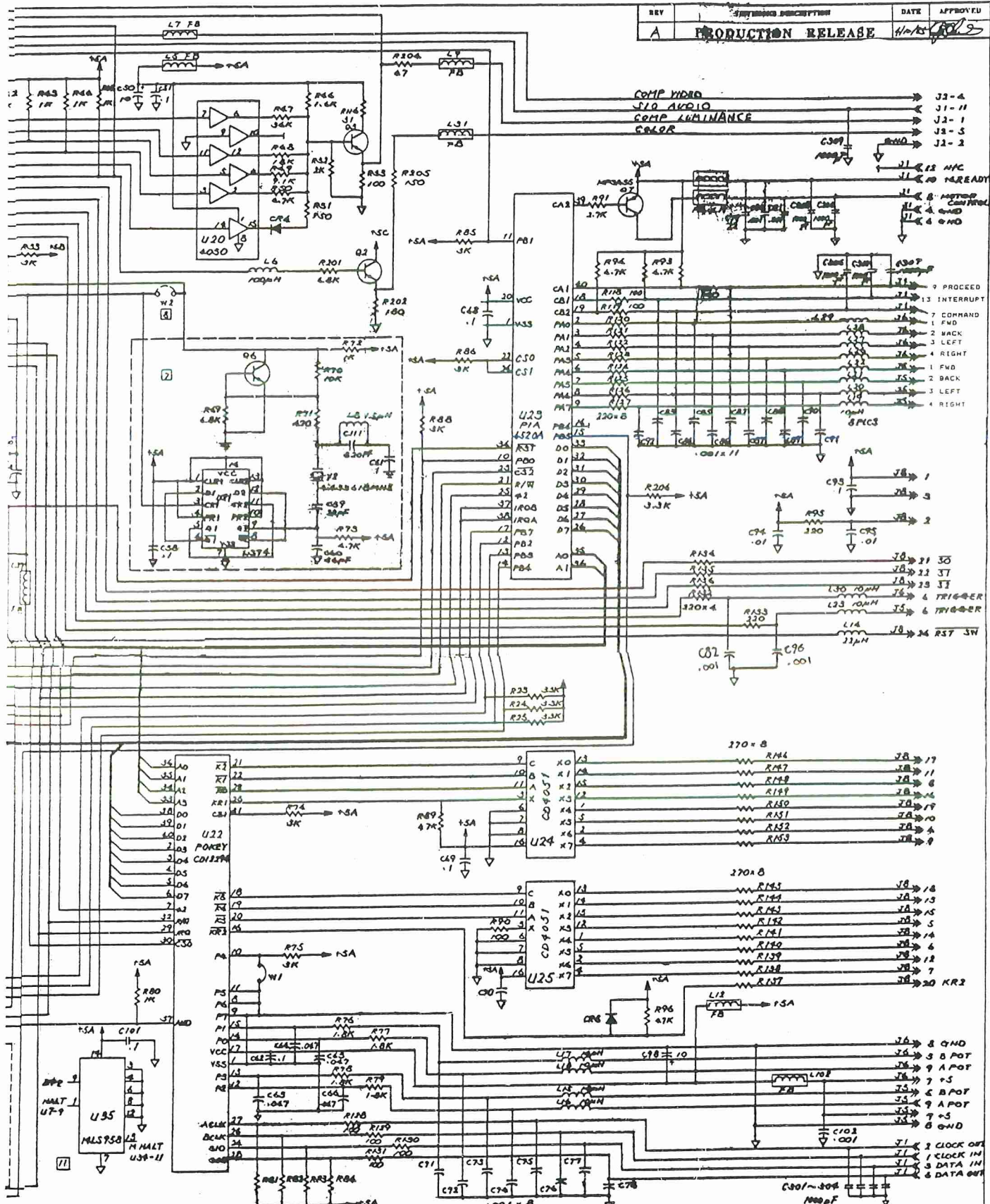


REV	DESCRIPTION	DATE	APPROVED
A	PRODUCTION RELEASE	11/85	[Signature]



SIGNATOR USED	
A1	J3, L4, J8, W2
PARTS NOT USED	
C4, 52-57	L4, 29-34
L7, 79, 100	33-44
100, 106-110	J3
121, 109	CR1

12. APPLICABLE PCB C070067

DESIGNER A303E	DRAWN BY M. K. [Signature]	DATE 11/85	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 3F 4-3-10, Toranomon Minato-ku, Tokyo 105
CHECKED CA070012	ENGINEER A. S. [Signature]	DATE 11/85	TITLE SCHEMATIC DIAGRAM (130XE HISC/PAL)
MATERIAL	APPROVED [Signature]	DATE 11/85	SHEET NO. D
FINISH	APPROVED [Signature]	DATE 11/85	DRAWING NO. C070065
			SCALE NONE
			SHEET 1 OF 1